

Serial No. 10/505,481

IN THE ABSTRACT:

Please amend the Abstract to read as follows:

A plasma display panel can to stabilize address properties. A front substrate (1) and a back substrate (2) are ~~disposed to~~ face each other, and to form a discharge space (3) ~~is formed and~~ which is partitioned by barrier ribs (11) ~~so as to form~~ priming discharge cells (16) and main discharge cells (12). ~~Forming~~ ~~priming electrodes (15) onto a~~ A dielectric layer (17) in is formed on the back substrate (2) on which the priming discharge cells cell (16) is present. ~~can secure the isolation voltage~~ ~~between data electrodes (10) and the priming electrodes (15),~~ ~~and can also secure the generation of a priming discharge prior~~ ~~to a main discharge~~ Insulation is ensured between a data electrode (10) and the priming electrode (15) because the latter is formed on the dielectric layer (17). One is able to generate a priming discharge before a main discharge.